	Type	L	#	Hits	Search Text	DBs	Time Stamp	Comment
1	BRS	L1		3	6144712.pn. or 6498823.pn. or 6674823.pn.	USPA T; US-P GPUB ; EPO; JPO	2004/09/1 1 13:19	
2	BRS	L2		210	(plural\$4 near2 clock\$3 or multi\$3 near2 clock\$3 or first adj2 clock\$1 with second adj2 clock\$3 or PLL or phase adj2 lock\$3 adj2 loop\$1) same (phase adj2 offset\$3 or phase adj2 difference or phase adj2 subtract\$4 or phase near2 error\$1 or phase near2 error\$1 or phase near2 estimat\$4) same (interpolat\$4 or upsampl\$4)	USPA T; US-P GPUB; EPO; JPO	2004/09/1 1 13:51	
3	BRS	L3		70	2 and 375/\$.ccls.	USPA T; US-P GPUB ; EPO; JPO	2004/09/1 1 13:23	
4	BRS	L8		0	2 and 340/\$.ccls.	USPA T; US-P GPUB ; EPO; JPO	2004/09/1 1 13:24	
5	BRS	L9		0	2 and 330/\$.ccls.	USPA T; US-P GPUB ; EPO; JPO	2004/09/1 1 13:24	
6	BRS	L4		8	2 and 455/\$.ccls.		2004/09/1 1 13:24	

	Error Definition	Er ro rs
1		0
2		0
3		0
4		0
5		0
6		0

•	Туре	L #	Hits	Search Text	DBs	Time Stamp	Comment
7	BRS	L5	9	2 and 370/\$.ccls.	USPA T; US-P GPUB ; EPO; JPO	2004/09/1 1 13:29	
8	BRS	L7	6	2 and 329/\$.ccls.	USPA T; US-P GPUB ; EPO; JPO	2004/09/1 1 13:36	
9 .	BRS	L6	13	2 and 341/\$.ccls.	USPA T; US-P GPUB ; EPO; JPO	2004/09/1 1 13:43	
10	BRS	L10	52	3 not (6 or 7 or 5 or 4)	USPA T; US-P GPUB ; EPO; JPO	2004/09/1 1 13:51	
11	BRS	L11	6	6539072.URPN.	USPA T	2004/09/1 1 13:49	
12	BRS	L12	18	("4338569" "4884041" "5180994" "5216302" "5399995" "5451894" "5485490" "5513327" "5532633" "5534805" "5550783" "5570054" "5614855" "5619541" "5712883" "5712884" "5764092" "6282253").PN.	1	2004/09/1 1 13:49	

	Error Definition	Er ro rs
7		0
8		0
9		0
10		0
11		0
12		Ο

•	Туре	L #	Hits	Search Text	DBs	Time Stamp	Comment
13	BRS	L13	28	(plural\$4 near2 clock\$3 or multi\$3 near2 clock\$3 or first adj2 clock\$1 with second adj2 clock\$3 or PLL or phase adj2 lock\$3 adj2 loop\$1) same (AFC or frequenc\$3 adj2 control\$5 or phase near2 correct\$4) same (interpolat\$4 near2 phase)	USPA T; US-P GPUB ; EPO; JPO	2004/09/1 1 14:15	
14	BRS	L14	73	(plural\$4 near2 clock\$3 or multi\$3 near2 clock\$3 or first adj2 clock\$1 with second adj2 clock\$3 or PLL or phase adj2 lock\$3 adj2 loop\$1) same (AFC or frequenc\$3 adj2 control\$5 or phase near2 correct\$4) same (interpolat\$4)	USPA T; US-P GPUB; EPO; JPO	2004/09/1 1 14:09	
15	BRS	L15	0	13 not 14	USPA T; US-P GPUB; EPO; JPO	2004/09/1 1 14:15	

	Error Definition	Er ro rs
13		0
	T.	
14	·	0
15		0